

# Applying Design Patterns for Communication Components

## Communicating CSE components for the Two-dimensional Wave Equation

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### Abstract

The Design Patterns for Communication Components is a collection of patterns related with a method for developing the communication sub-systems of parallel software systems. These design patterns take as input information *(a)* the architectural pattern of the overall parallel software system, *(b)* the memory organization of the parallel hardware platform, and *(c)* the type of synchronization required.

In this paper, it is presented the application of the Design Patterns for Communication Components along with the method for communicating the CSE components of the Two-dimensional Wave Equation. The method used here takes the information from the Problem Analysis and Coordination Design, applies a design pattern for the communication components, and provides elements about its implementation.

## 1 Introduction

Parallel programming is characterized by a growing set of parallel hardware architectures, programming paradigms, and parallel languages. This situation makes difficult to propose just a single approach containing all the details to design and implement communication components for all parallel software systems. Hence, the Design Patterns for Communication Components [15, 17] are proposed as an effort to help a programmer to design the communication components depending on particular characteristics and features of the communication to be carried out between the processing components, when designing a parallel program.

The Design Patterns for Communication Components focus on describing and refining the communication components of a parallel program, by describ-

ing common programming structures used for communicating, exchanging data or requesting operations, between processing components. Their application directly depends on the Architectural Pattern for Parallel Programming [13, 17] which they are part of, detailing a communication and synchronization function as a local problem, and providing a form as a local solution of software components for such a communication problem.

When designing the communication components of a parallel program, it is important to think carefully how communication and synchronization are to be actually carried out by those communication components. From the many descriptions about how to organize the communication components of a parallel program [12, 8, 1] the Design Patterns for Communication Components [15, 17] are proposed having the following advantages:

- The Design Patterns for Communication Components describe some common structures used to refine and detail the communications and synchronization required by an Architectural Pattern for Parallel Programming [13, 17]. From this point of view, their objective is to help the software designer or programmer by providing medium-scale descriptions of software compounds that are used to communicate parallel processing components. There has been an extensive research and development of such software compounds [12, 8, 1], but unfortunately, normally they have not been related or linked with overall structures of parallel programs.
- The Design Patterns for Communication Components, as any software pattern, are descriptions about how to relate a communication function (in run-time terms) with a coded form (in compile-time terms). In many parallel applications, communication components are designed so their run-time communication function is organized having little resemblance to the compile-time organization of code that performs it. In fact, both organizations are by far very independent from each other, making it difficult to notice how communication is performed by coded components. The Design Patterns for Communication Components attempt to connect both descriptions into a single description that provides both, dynamic and static information about those communication components.
- Design Patterns for Communication Components describe software sub-systems or sub-structures for data exchange and/or function call. As such, they are a guidance about how to find the set of software component that perform communication and synchronization between processing components. The communication between processing components of a parallel software system is a key for the success or failure of such a parallel software system. Hence, the Design Patterns for Communication Components are developed and classified based on (a) the architectural pattern used for the overall parallel software system, (b) the memory organization of the parallel hardware platform, and (c) the type of synchronization in the parallel programming language available. These three features strongly affect

the design and implementation of communication software components. Thus, the Design Patterns for Communication Components describe and document several generic and different software structures for communication components, referring to different kinds of architectural patterns (Parallel Pipes and Filters, Parallel Layers, Communicating Sequential Elements, Manager-Workers, and Shared Resource) [13, 17], the hardware platform memory organization (shared memory or distributed memory), and the different types of synchronization available in common programming languages (synchronous or asynchronous).

- Normally, the Design Patterns for Communication Components are described so their components are explained in Object-Oriented terms. This does not prevent that they can be developed using other different paradigms and programming languages (such as process or task oriented). Nevertheless, such a description allows to take into consideration and advantage Object-Oriented principles, such as the separation between interfaces and implementation, reuse, and modifiability of code [7, 18].
- The Design Patterns for Communication Components introduce communication structures as forms in which software components are assembled or arranged together in order to perform communication. The communication structures are represented by forms as regular organizations of software components, aiming to allow software designers to understand complex communication software sub-systems, and therefore, reducing their cognitive burden.
- Design Patterns for Communication Components are based on the common concepts and terms originally used for inter-process communication [5, 9, 2, 10, 3, 11, 4]. As such, these design patterns make use and provide elements to develop a terminology for designing communication components for parallel programs.

## 2 Specification of the System

In the paper “*Applying Architectural Patterns for Parallel Programming. Solving the Two-dimensional Wave Equation*” [16], the Communicating Sequential Elements Architectural Pattern has been selected as a viable solution for the Two-dimensional Wave Equation. Now, in order to apply the Design Patterns for Communication Components for developing the communication components for this example, some information related with the Communicating Sequential Elements Pattern and the parallel platform and programming language is required. This information is summarized as follows.

### 2.1 The Communicating Sequential Elements pattern

The algorithmic solution for the Two-dimensional Wave Equation is defined in terms of calculating the next position of all elements that compose the two-

dimensional surface, as ordered data. Each position is obtained almost autonomously. The exchange of data or communication should be between neighboring elements of the surface. So, the Communicating Sequential Elements (CSE) pattern has been chosen as an adequate solution for the Two-dimensional Wave Equation. The design of the parallel software system has been continued based on the Solution section of the CSE pattern, as briefly described as follows [14, 16].

- **Description of the coordination.** The CSE pattern describes a coordination in which multiple **Element** objects act as concurrent processing software components, each one applying the same wave operation, whereas **Channel** objects act as communication software component which allow exchanging position values between software components. No position values are directly shared among **Element** objects, but each one may access only its own private position values. Every **Element** object communicates by sending its position value from its local space to its neighboring **Element** objects, and receiving in exchange their position values. This communication is normally asynchronous, considering the exchange of a single position value at a time, in a one to one fashion. Therefore, the data representing the whole two-dimensional surface represents the regular logical structure in which data of the problem is arranged. The solution, in terms of a divided two-dimensional surface, is presented as a network that actually reflects this logical structure in the most transparent and natural form [14, 16].
- **Structure and dynamics**
  1. *Structure.* When applying the CSE pattern for the Two-dimensional Wave Equation, the same operation is applied simultaneously to obtain the next position values of each element. However, this operation depends on the partial results in its neighboring elements. Hence, the structure of the actual solution involves a regular, two-dimensional, logical structure, conceived from the surface of the original problem. Thus, the solution is presented as a two-dimensional network of elements that follows the shape of the surface. Identical components simultaneously exist and process during the execution time. An Object Diagram, representing the network of elements that follows the two-dimensional shape of the surface and its division into elements, is shown in Figure 1 [16].

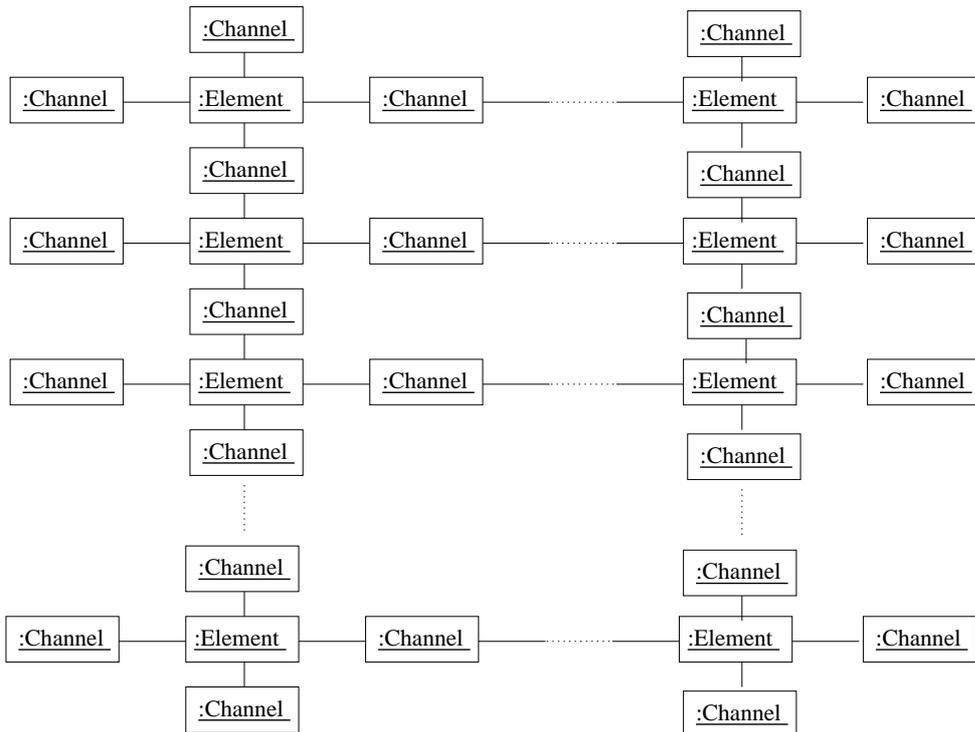


Figure 1: Object Diagram of CSE for solving the Two-dimensional Wave Equation.

2. *Dynamics.* A scenario to describe the basic run-time behavior of the CSE pattern for solving the Two-dimensional Wave Equation is shown as follows. Notice that all the elements, as basic processing software components, are active at the same time. Every element performs the same wave operation, as a piece of a processing network. However, for the two-dimensional case here, each element object communicates with its four neighbors as shown in Figure 2 [16].

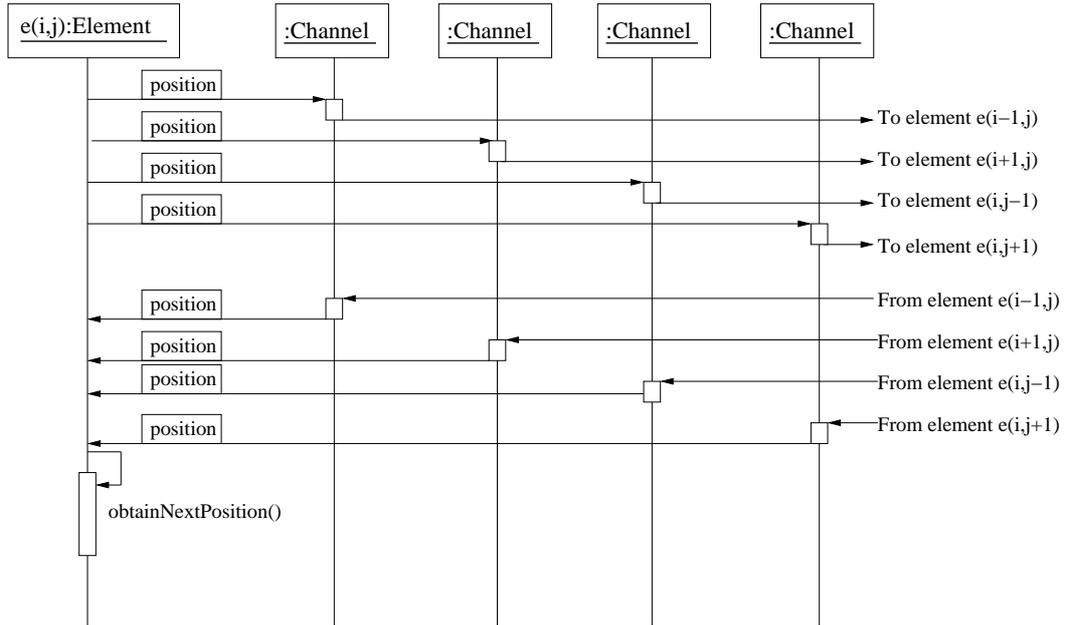


Figure 2: Sequence Diagram of CSE for communicating positions through channel components for the Two-dimensional Wave Equation.

The processing and communicating scenario is as follows [16]:

- Initially, consider only a single Element object,  $e(i,j)$ . At first, it exchanges its local position value with its neighbors  $e(i-1,j)$ ,  $e(i+1,j)$ ,  $e(i,j-1)$ , and  $e(i,j+1)$  through the adequate communication **Channel** components. After this,  $e(i,j)$  counts with the different positions from its neighbors.
- The position operation is simultaneously started by the  $e(i,j)$  component and all the other components of the surface.
- In order to continue, all components iterate as many times as required, exchanging their partial position values through the available communication channels.

- The process repeats until each component has finished iterating, and thus, finishing the whole Two-dimensional Wave Equation computation.
3. *Functional description of components.* The processing and communicating software components for solving the Two-dimensional Wave Equation using the CSE pattern are described as follows [16].
- **Element.** The responsibilities of an element, as a processing component, are to obtain the next position from the position values it receives, and make available its own position value so its neighboring components are able to proceed.
  - **Channel.** The responsibilities of every channel, as a communication component, are to allow sending and receiving position values, synchronizing the communication activity between neighboring sequential elements. Channel components are developed as the main design objective of this paper, in section “Communication Design” below.

## 2.2 Information about parallel platform and programming language

The parallel system available for this example is a SUN SPARC Enterprise T5120 Server. This is a multi-core, shared memory parallel hardware platform, with 18-CoreUltraSPARC T2, 1.2 GHz processors (capable of running 64 threads), 32 Gbytes RAM, and Solaris 10 as operating system. Applications for this parallel platform can be programmed using the Java programming language [13].

## 3 Communication Design – Specification of Communication Components

### 3.1 The scope

This section takes into consideration the basic information about the parallel hardware platform and the programming language used, as well as the CSE pattern as the selected coordination for solving the Two-dimensional Wave Equation. The objective is to look for the relevant information for choosing a particular design pattern as a communication structure.

Based on the information about the parallel platform (a shared memory multi-core computer), the programming language (Java) and the description of channels as communication software components for the CSE pattern presented in the previous section, the procedure for applying a Design Pattern for the Communication Components for the Two-dimensional Wave Equation problem is presented as follows [15, 17]:

1. *Consider the architectural pattern selected in the previous step.* From the CSE pattern description, the design patterns which provide communication components and allow the behavior as described by this architectural pattern for a coordination are the Shared Variable Channel pattern and the Message Passing Channel pattern [15, 17].
2. *Select the nature of the communicating components.* Considering that the parallel hardware platform to be used has a shared memory organization, the nature of the communicating components for such memory organization is considered to be shared variable.
3. *Select the type of synchronization required for the communication.* Normally, the communication between software components that compose an array communicated through point to point communication components makes use of an asynchronous communication. If a synchronous communications would be used, it is very likely that the processing software components would block, waiting for receiving position values from their counterpart. As every software component would be waiting, none would be receiving, leading to a deadlock situation. In this case, using a relaxation method such as Gauss-Seidel relaxation or the successive over-relaxation (SOR) at the coordination level would sort this out. Nevertheless, in the Problem Analysis it is stated that a Jacobi relaxation is to be used [16]. So, this makes it more important to make use of an asynchronous communication scheme which avoids sender waiting for their receivers.
4. *Selection of a design pattern for communication components.* Considering (a) the use of the CSE pattern, (b) the shared memory organization of the parallel platform, and (c) the use of asynchronous communications, therefore the **Shared Variable Channel pattern** is proposed here as the base for designing the communications between Sequential Elements. Let us consider the Context and Problem sections of this pattern [15, 17]:
  - **Context:** ‘A parallel program is being developed using the Communicating Sequential Elements architectural pattern as a domain parallelism approach in which the data is partitioned among autonomous processes (elements) as the processing components of the parallel program. The parallel program is developed for a shared memory computer. The programming language to be used counts with synchronization mechanisms for process communication such as semaphores, critical regions, or monitors’
  - **Problem:** ‘A sequential element needs to exchange values with its neighboring elements. Every data is locked inside each sequential element, which is responsible for processing that data and only that data’

From both these descriptions, it is noticeable that for the CSE pattern, on a shared memory parallel platform, and using Java as the programming

language, the choice for developing the communication components for this example is the **Shared Variable Channel pattern** [15, 17]. The use of a shared memory parallel platform implies using shared variables, and it is known that the Java programming language counts with the elements for developing semaphores or monitors. Moreover, the channels consider an asynchronous communication scheme between sender and receiver. Therefore, this completes the selection of the Design Pattern for Communication Components of the Two-dimensional Wave Equation. The design of the parallel software system continues using the Shared Variable Channel pattern's Solution section as a starting point for communication design and implementation.

### 3.2 Functional description of software components

This section describes each software component of the Shared Variable Channel pattern as the participant of the communication sub-system, establishing its responsibilities, input, and output [15, 17].

1. **Synchronization Mechanisms.** This kind of components is used to synchronize the access to the `Double` shared variables. Notice that they should allow the translation of `send()` and `receive()` operations into adequate operations for writing to and reading from the shared variables. Normally, synchronization mechanisms are used to keep the order and integrity of the shared data.
2. **Shared Variables.** The responsibility of the shared variables is to store the `Double` type that holds the position values exchanged by sequential elements. These shared variables are designer here as simple variables that buffer during communication, for actually achieving an asynchronous communication.

### 3.3 Structure and dynamics

This section takes information of the Shared Variable Channel design pattern, expressing the interaction between its software components that carry out the communication between parallel software components for the actual example [15, 17].

1. *Structure.* The structure of this pattern applied for designing and implementing channel communication components of the CSE pattern is shown in Figure 3 using a UML Collaboration Diagram [6]. Notice that the channel component structure allows an asynchronous, bidirectional communication between two sequential elements. The asynchronous feature is achieved by allowing an array of positions to be stored, so the sender does not wait for the receiver [15, 17].

2. *Dynamics*. This pattern actually emulates the operation of a channel component within the available shared memory, multi-core parallel platform. Figure 4 shows the behavior of the participants of this pattern for the actual example [15, 17].

In this scenario, a point to point, bi-directional, asynchronous communication exchange of position values of type Double is carried out, as follows:

- The `element(i)` sequential element sends its local `position` value by issuing a `send(position)` operation to the sending Synchronization Mechanism.
- This Synchronization Mechanism verifies if the `element(i+1)` sequential element is not reading the `position` shared variable. If this is the case, then it translates the sending operation, allowing a `write(position)` operation of the data item on `position`. Otherwise, it blocks the operation until the `position` can be safely written.
- When the `element(i+1)` attempts to receive the position value, it does so by issuing a `receive(position)` request to the Synchronization Mechanism. This function returns a `double` type representing the position value stored in the shared variable `position`. Again, only if its counterpart sequential element (here, `element(i)`) is not writing on `position`, the Synchronization Mechanism grants a `read(position)` operation from it, returning the requested `position` value. This achieves the send and receive operations between neighboring element elements.
- On the other hand, when data flows in the opposite direction, a similar procedure is carried out, but from `element(i+1)` to `element(i)`.

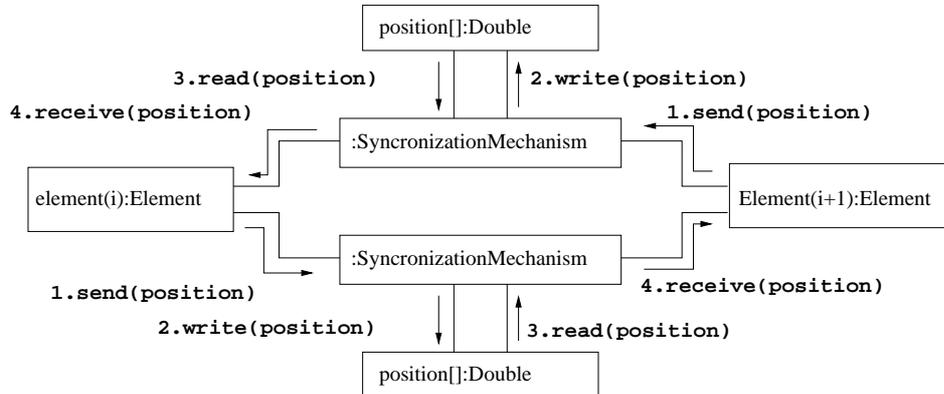


Figure 3: UML Collaboration Diagram of the Shared Variable Channel pattern used for asynchronously exchange position values between sequential components of the CSE solution to the Two-dimensional Wave Equation.

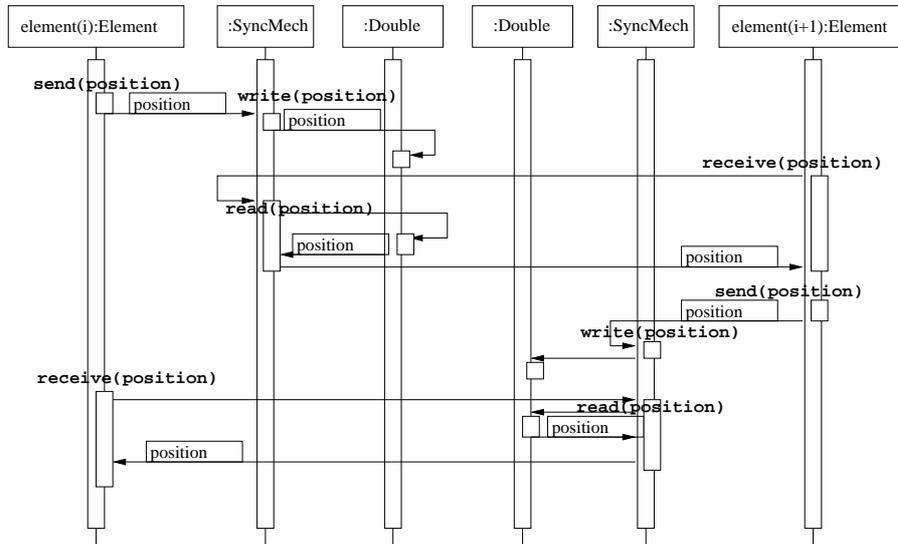


Figure 4: UML Sequence Diagram for the Shared Variable Channel pattern applied for exchanging position values between two neighboring sequential elements of the CSE solution for the Two-dimensional Wave Equation.

### 3.4 Description of the communication

The channel communication component, thus, acts as a single entity, allowing the exchange of information between processing software components. Given that the available parallel platform is a multi-core, shared memory system, the behavior of a channel component is modelled using shared variables. Thus, a couple of shared variables are used to implement the channel component as a bidirectional, shared memory communication means between elements. It is clear that such shared variables require to be safely modified by synchronizing read and write operations from the elements. Hence, the Java programming language provides the basic elements for developing synchronization mechanisms (such as semaphores or monitors). This is required to preserve the order and integrity of the transferred position values.

### 3.5 Communication Analysis

This section describes the advantages and disadvantages of the Shared Variable Channel pattern as a base for the communication structure proposed.

#### 1. Advantages

- A communication sub-structure based on the Shared Variable Channel pattern allows to keep the precise order of the exchanged position

values by considering a two directional FIFOs for its implementation, as well as synchronizing the access to both `Double` type shared variables.

- The communication sub-structure based on the Shared Variable Channel pattern allows for a point to point, bidirectional communication component.
- The use of Synchronization Mechanisms grants keeping the integrity of transferred position values, assuring that at any given moment only one element actual accesses any `Double` type shared variables.
- The use of shared variables implies that the implementation is particularly developed for a shared memory parallel platform.
- The Shared Variable Channel pattern allows the use of asynchronous communications between sequential elements by using the two `Double` type shared variables as two communication buffers.

## 2. Liabilities

- As the available parallel platform is a shared memory one, the communication speed tends to be similar to simple assignation operations over shared variable addresses. Communications are only delayed by the synchronization actions taken by the Synchronization Mechanisms in order to keep the integrity of the position values. Nevertheless, it is very little what can be done to improve communication performance in terms of programming. The only programming action that can be taken is to change the amount of processing of the sequential elements, which modify the granularity, tuning the communication speed.
- The implementation based on shared variables and synchronization mechanisms such as semaphores, conditional regions, or monitors, makes these communication sub-systems only suitable for shared memory platforms. If the parallel software system is considered to be ported to a distributed memory parallel platform, this would require replacing each Shared Variable Channel pattern by a Message Passing Channel pattern [15], and design and implement the communication sub-systems as indicated by this pattern.

## 4 Implementation

In this section, all the software components described in the Communication Design step are considered for their implementation using the Java programming language. Here, it is only presented the implementation of the communication sub-system, which interconnects processing components that implement the actual computation that is to be executed in parallel [16]. So, the implementation

is presented here for developing the channel as communication and synchronization components. Nevertheless, this design and implementation of the whole parallel software system goes beyond the actual purposes of the present paper.

## 4.1 Synchronization Mechanism

Based on the Java programming language, a basic description of a synchronization mechanism that controls the access to the position array is presented as follows:

```
import java.util.Vector;
class SynchronizationMechanism {
    private int numMessages = 0;
    private final Vector positions = new Vector();
    public final synchronized void write(double pos){
        ...
        numMessages++;
        positions.addElement(pos);
        ...
    }
    public final synchronized double read(){
        double pos = 0.0d;
        numMessages--;
        ...
        pos = positions.firstElement();
        positions.removeElementAt(0);
        return pos;
    }
}
```

The class `SynchronizationMechanism` presents two synchronized methods, `write()` and `read()`, which allow to safely modify the positions buffer and allows an asynchronous communication between `element` components. This class is used in the following implementation stage as the basic element of the channel components.

## 4.2 Communication components – Channels

Using the class `SynchronizationMechanism` from the previous section, here it is used as the synchronization mechanism component as described by the Shared Variable Channel pattern, in order to implement the class `Channel`, as follows:

```
public final class Channel {
    private SynchronizationMechanism m0 = null;
    private SynchronizationMechanism m1 = null;
    public Channel(){
        m0 = new SynchronizationMechanism();
        m1 = new SynchronizationMechanism();
    }
}
```

```

    }
    public void send0(Channel c, double pos){
        if(pos == null) throw new NullPointerException();
        m0.write(pos);
    }
    public void send1(Channel c, double pos){
        if(pos == null) throw new NullPointerException();
        m1.write(pos);
    }
    public double receive0(Channel c){
        return m0.read();
    }
    public double receive1(Channel c){
        return m1.read();
    }
}

```

Each channel component is composed of two synchronization mechanisms which allow the bi-directional flow of data through the channel. In order to keep the direction of each message flow, it is necessary to define two methods for sending and other two methods for receiving, and keep attention about the flow of messages. Each method distinguishes on which synchronization mechanism of the channel the message is written. This allows that the channel is capable of allowing a simultaneous bi-directional flow. In the present example, this is used to enforce the use of the Jacobi relaxation [16]. In fact, using (a) a channel communication structure with two-way flow of data, (b) making each one of them asynchronous, and later, (c) taking care on the communication exchanges between element components, are all design provisions for avoiding any potential deadlock. In parallel programming, it is generally advised that during design, all provisions should be taken against the possibility of a deadlock.

Moreover, in case of modifying the present implementation for executing on a distributed memory parallel system, it would be necessary only to substitute the implementation of the class `Channel`, but using the Message Passing Channel pattern [15] as a base for its definition.

## 5 Summary

The Design Patterns for Communication Components are applied here along with a method for selecting them, in order to show how to apply a design pattern that copes with the requirements of communication present in the Two-dimensional Wave Equation problem. The main objective of this paper is to demonstrate, with a particular example, the detailed design and implementation that may be guided by a selected design pattern. Moreover, the application of the Design Patterns for Communication Components and the method for selecting them is proposed to be used during the Communication Design and Implementation for other similar problems that involve the calculation of differ-

ential equations for a two-dimensional problem, executing on a shared memory parallel platform.

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